INSTRUCTIONS

GEK-49841B

Supersedes GEK-49841A

STATIC PHASE
MHO DISTANCE RELAY
TYPE SLY62A

GENERAL ELECTRIC

CONTENTS

STATIC PHASE MHO DISTANCE RELAY
TYPE SLY62A

DESCRIPTION

The SLY62A is a second zone, three phase, static phase distance relay. The SLY62A is a rack mounted unit, two rack units high. It requires other fault detecting relays, an SSA power supply, SLA logic unit(s), SLAT output unit(s), and a test panel to provide a complete scheme of protection for a transmission line.

APPLICATION

The SLY62A relay utilizes a two input phase angle comparator for the phase distance measurement. The two inputs for the phase A-B measurements are:

(a) $(I_A - I_B)\ Z_{R1} - T\ V_{AB}$ Operating Quantity

(b) $V_{AB} + K\ V_{AB1}$ Polarizing Quantity

where

I_A and I_B are the currents in the faulted phases

Z_{R1} is the base reach impedance with a selectable impedance angle of 85 degrees and 75 degrees

$T\ V_{AB}$ is the faulted phase-to-phase voltage, multiplied by the restraint tap T.

$V_{AB} + KV_{AB1}$ is the faulted phase-to-phase voltage plus K times the positive sequence component of the faulted phase-to-phase voltage. K is a design constant equal to 0.3 per unit.

The use of the positive sequence component of voltage in the polarizing signal results in a relay characteristic of the "variable mho" type, that is, the characteristic expands as the source impedance behind the relay increases, providing an automatic accommodation of arc impedance. The positive sequence component of voltage in the polarizing signal also improves the directional integrity of the distance measurement.

The SLY62A has an adjustable characteristic which is adjusted by means of the timer setting on the characteristic timer. For short lines a circular characteristic is recommended, but for longer lines, lines with unusually heavy load transfer, or three terminal lines where very large reach settings are required, a lens shaped characteristic is recommended.

RATINGS

This relay is designed for use in an environment where the air temperature outside the relay case is between -20°C and +65°C.

These instructions do not purport to cover all details or variations in equipment nor to provide for every possible contingency to be met in connection with installation, operation or maintenance. Should further information be desired or should particular problems arise which are not covered sufficiently for the purchaser's purposes, the matter should be referred to the General Electric Company.

To the extent required the products described herein meet applicable ANSI, IEEE and NEMA standards; but no such assurance is given with respect to local codes and ordinances because they vary greatly.

Forms of the SLY62A are available for either 50 or 60 hertz applications.

Forms of this relay are available with current circuits rated for either five amperes or one ampere for continuous duty,with a one second rating of 300 or 60 amperes respectively.

The potential circuits are rated for 120 volts.

The relay requires a ±15 VDC power source which may be obtained from type SSA power supplies.

Refer to the unit nameplate for the frequency and current ratings for a particular relay.

RANGES

The SLY62A relay has an adjustable reach of 0.1 to 30 ohms for the five ampere rated relay or 0.5 to 150 ohms for the one ampere rated relay.

Current input connections to establish the basic ohmic tap of one and three ohms line-to-neutral (5 ampere relay) or 5 and 15 ohms (1 ampere relay) are available at the current input terminals. Restraint taps in the voltage circuit range between 10 and 100 percent in one percent increments. In addition to the current input taps, the relay has a selectable base reach multiplier of 1.0, 0.5, 0.2 or 0.1 per unit.

The relay has a base reach angle which can be adjusted for 85 or 75 degrees.

The polarizing voltage has an adjustable phase shift relative to the operating quantity of zero or fifteen degrees lead.

BURDENS

The maximum potential burden per phase, measured at 120 volts rms is:

60 Hz Relay	50 Hz Relay	
0.35	0.45	Volt-ampere
0.28	0.34	Watt
0.20	0.29	Var

The maximum phase current burden per phase is:

Five Ampere Relay			One Ampere Relay		
1 or 3Ω Tap			5 or 15Ω Tap		
Z:	$0.026\angle 7.3°$	Ohms	Z:	$0.117\angle 1.6^{\circ}$	Ohms
R:	0.025	Ohms	R:	0.117	Ohms
X:	0.003	Ohms	X:	0.003	Ohms

The maximum burdens that the logic circuits present to the power supply are:

0.254	Ampere to the +15 VDC supply
0.119	Ampere to the -15 VDC supply

SENSITIVITY

Sensitivity is defined as the steady state rms voltage or current, measured at the relay terminals, required for a particular quantity to cause the relay to operate if all input quantities are in the optimal phase relationship. The nominal sensitivities for the signal quantities in the SLY62A relay are as follows:

OPERATING CIRCUIT SENSITIVITY

For phase pair A-B, the current sensitivity can be determined from the relationship:

$$(I_A - I_B)Z_{R1} = \frac{0.14}{1-X}$$

where

$$X = \frac{\text{Actual Relay Reach}}{\text{Nominal Relay Reach}}$$

For example, if $Z_{R1} = 3\Omega$ and $X = 0.9$, then:

$$(I_A - I_B) = 0.47 \text{ ampere}$$

For a phase-to-phase fault, $I_A = (I_A-I_B)/2$ or 0.23 ampere. For a three phase fault, $I_A = (I_A-I_B)/\sqrt{3}$ or 0.27 ampere.

POLARIZING CIRCUIT SENSITIVITY

Sensitivity is one percent of rated voltage.

OPERATING PRINCIPLES AND CHARACTERISTICS

GENERAL

The mho characteristic is obtained by converting relay currents into voltage signals (IZ), combining these IZ signals with signals proportional to the line voltage (V), and measuring the angle between the appropriate combinations to obtain the desired characteristic.

Currents are converted into IZ signals by means of transactors (TE, TG and TJ) which are air gap reactors with secondary windings. The transactors are tapped on the primary to provide the basic ohmic tap selection of one or three ohms (five ampere rating) and five of fifteen ohms (one ampere rating).

The Z of the IZ quantity is the transfer impedance of the transactor and is equal to V_{OUT}/I_{IN}. The transactor secondaries have loading resistors across them. These resistors provide the desired angle between V_{OUT} and I_{IN}. This angle determines the base reach angle of the relay.

The mho distance characteristic is obtained by comparing the phase angle between the quantities (IZ-TV) and V_{pol} where V is the phase-to-phase voltage at the relay, V_{pol} is the polarizing voltage, I is the phase current, Z is the relay base reach and T is the voltage restraint tap. For a circular characteristic, relay operation occurs when the angle between (IZ-TV) and V_{pol} is less than or equal to 90 degrees.

RELAY REACH

The base reach of the relay (Z_{R1}) is determined by the Basic Ohmic Tap (BOT) and the Base Reach Multiplier Tap (BRM). The base reach is equal to the product of the basic ohmic tap and the base reach multiplier tap:

$$Z_{R1} = (BOT) \cdot (BRM) \qquad \text{(Eq 1)}$$

The reach of the MT/MOB function at the base reach angle is given by the expression:

$$Z_R = \frac{Z_{R1} \times 100}{T} \qquad \text{(Eq 2)}$$

or

$$Z_R = \frac{BOT \times BRM \times 100}{T} \qquad \text{(Eq 3)}$$

where:

Z_R is the reach at the base reach angle in line-to-neutral ohms

T is the voltage restraint tap setting in percent

BOT is the basic ohmic tap in line-to-neutral ohms

BRM is the base reach multiplier in per unit

Z_{R1} is the base reach of the relay in line-to-neutral ohms as defined in equation 1.

The reach of the relay is inversely proportional to the voltage restraint tap setting. The maximum reach of the relay (30 or 150 ohms depending upon the relay rating) is obtained with the suggested minimum restraint tap setting of ten percent.

ANGLE OF MAXIMUM REACH

The relay base reach angle is adjustable by means of links on the rear of the unit. The base reach angle can be set for either 85 or 75 degrees.

The polarizing quantity used to develop the mho characteristic can be phase shifted relative to the operating quantity. The phase shift can be set for either zero of 15 degrees lead. The 15 degree setting results in a clockwise shift in the angle of maximum reach away from the base reach angle. The angle of maximum reach is equal to the base reach angle minus the polarizing phase shift. The 15 degree setting increases the reach at the relay angle of maximum reach by the factor 1/cos (15^o) which is equal to 1.035.

PHASE ANGLE MEASUREMENT

The quantities IZ,-TV, and V_{pol} are supplied to a summing amplifier-filter card. This card sums IZ and -TV and filters extraneous frequencies from the quantities (IZ-TV) and V_{pol}. Each of the resulting signals is then amplified to produce two square wave outputs. The square waves are applied to a coincidence logic circuit which establishes the coincidence of the same instantaneous polarity of the square waves. The output of the coincidence logic is a rectangular pulse with a duration which is proportional to the phase angle between (IZ-TV) and V_{pol}.

The coincidence logic output is applied to a timing circuit which produces an output whenever the pulse width exceeds a preset duration. If the timer is set for 90 degrees (4.17 milliseconds on a 60 Hz base, 5.0 milliseconds on a 50 Hz base) a circular R-X characteristic is obtained. If the timer is set for less than 90 degrees, an expanded circle (tomato shaped) characteristic is obtained. If the timer is set for more than 90 degrees, a contracted circle (lens shaped) characteristic is obtained.

CALCULATION AND CHOICE OF SETTINGS

Assume that the line to be protected is approximately 70 miles long and has primary impedances as follows:

$$* \; Z_1' = 42\angle 83^o$$

Assume CT ratio is 1000/5 and PT ratio is 2000/1.

$$* \; Z_1' \text{ sec} = 42 \left(\frac{1000}{5}\right) \left(\frac{1}{2000}\right) = 4.2\angle 83^o$$

*

For short to medium length lines, that is, lines less than 100 miles long, the SLY62A relay is set as follows;

(a) The base reach angle is selected based on the positive sequence impedance angle of the line; for line angles above 80 degrees, select the 85 degree base reach angle tap. For line angles 80 degrees and below, select the 75 degree base reach angle tap. For the sample line, select the 85 degree tap.

(b) A typical relay reach (Z_R) for positive sequence line angles of 65 degrees and above is 175 percent of the positive sequence line impedance. For line angles below 65 degrees, refer to the local General Electric Sales Office for suggested settings. The base reach tap (Z_{R1}) is typically selected to be as large as possible, but still less than the relay reach (Z_R). This gives the maximum current sensitivity. The base reach tap (Z_{R1}) is the product of two factors: the basic ohmic tap (BOT) and the base reach multiplier (BRM). The basic ohmic taps available are one and three ohms; the base reach multiplier taps are 0.1, 0.2, 0.5 and 1.0. The available base reach settings are given in the following table.

Z_{R1}	BOT	BRM
0.1	1	0.1
0.2	1	0.2
0.3	3	0.1
0.5	1	0.5
0.6	3	0.2
1.0	1	1.0
1.5	3	0.5
3.0	3	1.0

For the sample line, the suggested relay reach setting, Z_R, is 1.75 (Z_{1L}) or 1.75 (4.2) or Z_R = 7.35 ohms. The suggested base reach (Z_{R1}) is three ohms. This results from a BOT of three ohms and a BRM of 1.0.

(c) After Z_{R1} has been selected, T, the restraint tap setting, can be calculated in accordance with the formula

$$Z_R = \frac{Z_{R1} \times 100}{T} \quad \text{or} \quad T = \frac{Z_{R1} \times 100}{Z_R}$$

For the sample line

$$* \; T = \frac{3 \times 100}{7.35} = 40.8 \text{ or } 41 \text{ percent}$$

(d) The phase shift tap is typically set on the zero degree tap for normal applications involving lines less than 100 miles long.

(e) For lines less than 100 miles long the characteristic timer setting is typically the factory * setting, namely, 120 degrees, 90 degrees/110 degrees. For 60 hertz applications, this is approximately 5.5, 4.2/5 milliseconds, and for 50 hertz, 6.6, 5/6 milliseconds.

Longer lines, such as those over 100 miles, and particularly those with heavy load transfer, may require modification of the settings for optimum performance. Refer to the nearest G-E Sales Office for suggested setting modifications.

MOB TIMER SETTING

The MOB characteristic timer setting should be set in conjunction with the out-of-step blocking timer in the SLA. The MOB characteristic should be sufficiently larger than the tripping relay characteristics it is blocking, to permit the OSB timer to time out during the period that the fastest swing impedance is inside the MOB characteristic but outside the tripping characteristic. Refer to the appropriate logic description for further discussion.

CONSTRUCTION

The type SLY62A relay is packaged in a metal enclosure designed for mounting on a 19 inch rack. The relay is two rack units high (one rack unit is 1-3/4 inches). The outline and mounting dimensions are shown in Fig. 1. The relay contains the magnetics and tap blocks for setting the base reach and the percent restraint. It also contains the printed circuit cards for developing three phase mho distance * characteristics. The relay has a hinged front cover and a removable top cover.

The setting of the basic ohmic tap is accomplished by the connection of the input currents to the YC terminal board on the rear of the relay. The connection points for the basic ohmic taps are shown n the table on the internal connection diagram of Fig. 2.

The voltage restraint tap blocks are located on the front of the unit at the left-hand side. Refer o the component location diagrams of Fig. 3. The voltage restraint tap settings are made by jumpers with aper tip pins on the end. In the accessory kit accompanying each equipment there are two special tools upplied for use with these pins. One is an insertion tool and the other is an extraction tool. In order o achieve a proper connection and to prevent damage to the pins, it is essential that these tools be used. wo tap blocks are used per phase: one is for the ten percent tap and one is for the one percent tap. The oltage restraint tap setting is the sum of the one and ten percent settings.

The relay also contains printed circuit cards which are located to the right of the tap blocks. The rinted circuit cards are identified by a code number such as F125, C106, T133 or P102 where F designates ilter, C designates coincidence, T designates time delay, and P designates processing. The printed ircuit cards plug in from the front of the unit. The sockets are identified by letter designations r "addresses" (D, E, F etc.) which appear on the guide strips in front of each socket, on the component ocation diagram, on the internal connection diagram and on the printed circuit card itself.

RECEIVING, HANDLING AND STORAGE

These relays will normally be supplied as a part of a static relay equipment, mounted in a rack or abinet with other static relays and test equipment. Immediately upon receipt the static relay equipment hould be unpacked and examined for any damage sustained in transit. If injury or damage resulting from ough handling is evident, file a damage claim at once with the transportation company and promptly notify the nearest General Electric Sales Office.

Reasonable care should be exercised in unpacking the equipment. If the equipment is not to be installed immediately, it should be stored indoors in a location that is free from moisture, dust, metallic chips and severe atmospheric contaminants.

Just prior to final installation the shipping support bolt should be removed from each side of all relay units, to facilitate possible future unit removal for maintenance. These shipping support bolts are approximately eight inches back from the relay unit front panel. Static relay equipment, when supplied in swing rack cabinets, should be securely anchored to the floor or to the shipping pallet to prevent the equipment from tipping over when the swing rack is opened.

INSTALLATION TESTS

CAUTION

THE LOGIC SYSTEM SIDE OF THE DC POWER SUPPLY USED WITH MOD III STATIC RELAY EQUIPMENT IS ISOLATED FROM GROUND. IT IS A DESIGN CHARACTERISTIC OF MOST ELECTRONIC INSTRUMENTS THAT ONE OF THE SIGNAL INPUT TERMINALS IS CONNECTED TO THE INSTRUMENT CHASSIS. IF THE INSTRUMENT USED TO TEST THE RELAY EQUIPMENT IS ISOLATED FROM GROUND, ITS CHASSIS MAY HAVE AN ELECTRICAL POTENTIAL WITH RESPECT TO GROUND. THE USE OF A TEST INSTRUMENT WITH A GROUNDED CHASSIS WILL NOT AFFECT THE TESTING OF THE EQUIPMENT. HOWEVER, CONNECTION TO THE EQUIPMENT, SUCH AS A TEST LEAD INADVERTENTLY DROPPING AGAINST THE RELAY CASE, MAY CAUSE DAMAGE TO THE LOGIC CIRCUITRY. IT IS A GOOD TEST PROCEDURE TO CONNECT THE REFERENCE LEAD OF A TEST INSTRUMENT TO RELAY REFERENCE BEFORE CONNECTING THE SIGNAL LEAD.

GENERAL

The test points (TP1, TP2, etc.) shown on the internal connection diagram are connected to instrument test jacks on a test card in card location T. TP1 is located at the top of the card and is connected to relay reference. TP10 is located at the bottom of the card and is connected to +15 VDC. Output signals are measured with respect to the relay reference (TP1). Logic signals are approximately +15 VDC for the ON or LOGIC ONE condition, and between zero and +1 VDC for the OFF or LOGIC ZERO condition. Filter card outputs are square waves which shift from +15V to -15V.

Any of the input/output pins on the printed circuit boards can be monitored by using the test card adapter as described in the printed circuit card instruction book GEK-34158. The logic signals can be monitored with an oscilloscope, a portable high impedance voltmeter or the voltmeter on the equipment test panel. When the test panel meter is supplied, it will normally be connected to relay reference. Placing the test lead to the proper test point will connect the meter for testing. When time-delay cards are to be adjusted or checked, an oscilloscope which can display two traces simultaneously and which has a calibrated horizontal sweep should be used.

The relay contains printed circuit cards with trimmer potentiometers mounted on them. Some of these potentiometers are factory set and sealed. These potentiometers should not be readjusted.

Before testing the relay, the trip outputs from the associated type SLAT relay should be opened to prevent inadvertent tripping of the breakers.

Input currents and voltages may be supplied to the relay through Type XLA test plugs placed in the test receptacles on the equipment test panel. Reference to the elementary diagram for the static relay equipment will provide information concerning equipment inputs. All units of a given terminal have been calibrated together at the factory and will have the same summary number on the unit nameplates. These units should be tested and used together.

NECESSARY ADJUSTMENTS

The following checks and adjustments should be made by the user in accordance with the procedures given under DETAILED TESTING INSTRUCTIONS before the relay is put into service. The necessary set points may be calculated following the procedures under CALCULATION AND CHOICE OF SETTINGS. The adjustments should be made in the order shown.

1. Base reach setting (Z_{R1})
    a) Basic ohmic tap selection (BOT)
    b) Base reach multiplier selection (BRM)
2. Voltage restraint tap setting (T)
3. Angle of maximum reach setting
    a) Base reach angle
    b) Polarizing voltage phase shift
4. Characteristic timer setting
5. Overall check of the MT/MOB function

DETAILED TESTING INSTRUCTIONS

BASE REACH SETTING

The basic ohmic tap (BOT) is determined by the terminals to which the relay input currents are connected to the unit. The correct input terminals for the various taps are given in Table I.

TABLE I

CURRENT TAP	I_A		I_B		I_C	
	IN	OUT	IN	OUT	IN	OUT
1 or 50 ohm	YC4	YD8	YC6	YD9	YC8	YD10
3 or 15 ohm	YC3	YD8	YC5	YD9	YC7	YD10

The base reach multiplier selector is located on the card in location E. The adjustment is accomplished by a four position jumper block. In each position the gain of the IZ circuit for each phase is set to the proper value of base reach multiplier. The base reach multiplier for each jumper position on the card is shown in Table II. The relay base reach is given by equation 1.

TABLE II

JUMPER POSITION	BASE REACH MULTIPLIER (BRM)
B	1.0
C	0.5
D	0.2
E	0.1

VOLTAGE RESTRAINT TAP SETTING

The voltage restraint tap setting (T) is accomplished on tap blocks located on the front of the relay. Two separate tap blocks are provided for each phase pair. The setting consists of a ten percent tap and a one percent tap. A restraint tap setting of 57 percent would consist of a ten percent setting of 50 and a one percent setting of 7. Only the special tools supplied with the relay should be used to change the tap setting.

ANGLE OF MAXIMUM REACH SETTING

The base reach angle is adjusted by means of links on the rear of the relay. The angle can be set for 75 or 85 degrees.

In order to set the polarizing voltage phase shift, it is necessary to adjust the X option plus on the filter cards in card locations H, J and K. The setting must be made on each of the three cards. The polarizing voltage phase shift for each position is shown in Table III.

TABLE III

PLUG X SHORTING ARRANGEMENT	DEGREES OF LEADING PHASE SHIFT
2 to 3 and 4 to 5	0
1 to 2 and 3 to 4	15

The polarizing phase shift may be checked and, if necessary, adjusted by using the test circuit of Fig. 5 and the appropriate connections of Table IV. Note that only voltage is applied to the relay for this test. The following procedure is recommended:

1) Start with the phase A connections of Table IV. Set the voltage restraint tap for 100 percent. Adjust the voltage for 30 Vrms.

TABLE IV

PHASE PAIR	FIG. 5 CONNECTIONS A	B	JUMPER POINTS	FILTER CARD LOCATION
A-B	YD2, YD7	YD3, YD4	YD5 to YD6	H
B-C	YD4, YD3	YD5, YD6	YD2 to YD7	J
* C-A	YD6, YD5	YD7, YD2	YD3 to YD4	K

2) Observe the output at pins 8 and 9 of the filter card in location H for phase A-B. The following phase angle measurements should be made using a dual trace oscilloscope with a calibrated sweep sufficiently fast to provide an accurate measurement, and with both traces carefully zeroed on the center line. Refer to Fig. 6 for sample waveforms.

3.A) Zero Degree Polarizing Phase Shift
The square waves at pins 8 and 9 of the filter card should be exactly 180 degrees out of phase, i.e. the zero crossings of each trace should coincide at the center line and the traces should have opposite slopes. The phase shift between pin 8 and pin 9 may be adjusted by potentiometer P71 on the filter card.

* Indicates revision

B) Fifteen Degree Polarizing Phase Shift
The square wave at pin 9 is now shifted 15 degrees in the leading direction (toward the left hand edge of the screen.) The zero crossings of the square waves should now occur 15 degrees apart (0.694 milliseconds on a 60 Hz base, 0.833 milliseconds on a 50 Hz base). Refer to Fig. 6.

The phase shift between pins 8 and 9 may be adjusted by potentiometer P71 on the filter card.

4. Repeat steps 1, 2 and 3 for phase pairs B-C and C-A. Observe the outputs of the filter card associated with the phase under test. Use the appropriate connections from Table IV.

CHARACTERISTIC TIMERS SETTINGS

The pickup setting of the characteristic timer affects the shape of the MT characteristic as plotted on an R-X diagram. Increasing the pickup time narrows the characteristic, decreasing the pickup time widens the characteristic. The reset time delay (dropout time) provides an overlap of the next half cycle measurement and in the case of the MOB function, provides a time-delay dropout before resetting after the input signal is removed. The inputs to the characteristic timers are chains of pulses (one per half cycle) from the coincidence logic cards. The outputs of the timers are DC logic signals. The timer settings are discussed in the section CALCULATION AND CHOICE OF SETTINGS.

The timers used for the MT function are integrating characteristic timers. These timers are typically listed on the overall logic as T1, T2/T3 where T1 is the pickup time in milliseconds on a step D-C input, T2 is the pulse width which will cause the timer to pick up with one pulse applied per half cycle and T3 is the dropout delay. The operation of the integrating characteristic timers is discussed in the printed circuit card instruction book, GEK-34158.

The D-C pickup (T1) and the dropout (T3) of the MT characteristic timer may be set using the test circuit of Fig. 4. Before testing the timer with this circuit, the card which normally supplies the input to the timer must be removed (refer to Table V). Opening the normally closed contact of Fig. 4 causes the output to step to +15VDC after the pickup delay of the timer. To increase the pickup delay, turn the upper potentiometer (P1) on the timer card clockwise. Closing the contact causes the timer to drop out (step to less than one VDC) after the reset delay setting of the card. To increase the reset delay, turn the second potentiometer (P2) clockwise. The pulse pickup mode of the timer may be observed while plotting the characteristic as described in OVERALL CHECK OF THE MT CHARACTERISTIC. The applied voltage and current as well as the phase angle between them, can be adjusted to vary the pulse width of the timer input.

TABLE V

PHASE	TIMER UNDER TEST	REMOVE CARD
A-B	P	L
B-C	R	M
C-A	S	N
MOB	G	L, M, and N

OVERALL CHECK OF THE MT/MOB FUNCTION

The test circuit of Fig. 5 should be used to check the operation of the MT/MOB function. Use the connections of Table VI for the one or five ohm basic ohmic tap or Table VII for the three or fifteen ohm basic ohmic tap.

TABLE VI

PHASE PAIR	FIGURE 5 CONNECTIONS - 1 OR 5 OHM TAP						
	A	B	C	D	E	F	JUMPER POINTS
A-B	YD2, YD7	YD3, YD4	YC4	YD8	YD9	YC6	YD5 to YD6
B-C	YD4, YD3	YD5, YD6	YC6	YC9	YD10	YC8	YD2 to YD7
C-A	YD6, YD5	YD7, YD2	YC8	YD10	YD8	YC4	YD3 to YD4

TABLE VII

PHASE PAIR	FIGURE 5 CONNECTIONS - 3 OR 15 OHM TAP						
	A	B	C	D	E	F	JUMPER POINTS
A-B	YD2, YD7	YD3, YD4	YC3	YD8	YD9	YC5	YD5 to YD6
B-C	YD4, YD3	YD5, YD6	YC5	YD9	YD10	YC7	YD2 to YD7
C-A	YD6, YD5	YD7, YD2	YC7	YD10	YD8	YC3	YD3 to YD4

Relay Base Reach Angle and Reach Check

The following procedure is recommended to check the base reach angle (A) and the relay reach setting.

1) Use the test circuit of Fig. 5, starting with the phase A-B connections from Table VI or VII.

Set the test current for a current equal to or greater than that specified in Table VIII. Currents greater than twice rated should not be continuously applied to the relay. Currents greater than four times rated should not be applied to the relay. Currents between two and four times rated should not be applied longer than five minutes with an off time of at least five minutes.

TABLE VIII

BASE REACH OHMS	RECOMMENDED MINIMUM TEST CURRENT (AMP) FOR LESS THAN 2% PULL BACK	
	FIVE AMPERE RELAY	ONE AMPERE RELAY
3.0	2	0.4
1.5	5	1.0
1.0	5	1.0
0.6	10	2.0
0.5	10	2.0
0.3	20	4.0
0.2	20**	4.0**
0.1	20**	4.0**

**Less than 5 percent pull pack

2) Adjust the phase angle for the nominal base reach angle (75 or 85 degrees)

3) Observe the output at pin 8 of the appropriate filter card (card location H for phase pair A-B, J for B-C, or K for C-A)

4) Lower the voltage to the value given by the following expression:

$$V_T = 2 \times I_T \times BOT \times BRM \times \frac{100}{T} \qquad \text{(Eq 4)}$$

where:

BOT is the basic ohmic tap

BRM is the base reach multiplier

I_T is the test current

T is the voltage restraint tap setting in percent

V_T is the operating voltage at the base reach angle.

5) As the voltage is lowered observe the output of the MT function: TP5 for phase pair A-B, TP7 for B-C or TP9 for C-A. At the point where MT picks up, a slight adjustment of the phase angle and voltage should cause the square wave at pin 8 of the filter card to come out of saturation. A further adjustment will cause the signal at pin 8 to be reduced to a null voltage consisting of only third and fifth harmonics. The angle on the phase angle meter is the base reach angle and should be within two degrees of the nominal setting. The voltage should be within five percent of the value given by equation 4.

6) A vernier adjustment on the reach is provided on the filter card (position H, J, or K). Turning P10 on the appropriate filter card clockwise increases the voltage required to null the MT function, thereby increasing the relay reach.

7) Repeat steps 1 through 6 for phase pairs B-C and C-A using the connections from Table VI or VII.

Complete MT Function Check

The following procedure is recommended to provide an overall check of all the adjustments included in the MT function. It is only necessary to check the relay reach at two angles other than the base reach angle.

1) Use the test circuit of Fig. 5. Start with the A-B phase pair connections from Table VI or VII. Set the current for the desired level (Table VIII). Monitor the output at TP5 for phase pair A-B, TP7 for B-C, or TP9 for C-A.

2A) Pickup for angles less than or equal to the base reach angle.

a) Adjust the phase shifter to obtain a phase angle 30 degrees less than the base reach angle

b) Lower the applied voltage and check for an MT output within five percent of the voltage given by the expression:

$$V_D = V_T \times \frac{\text{SIN } (D-A + B + 180 - C)}{\text{SIN } (B + 180 - C)} \qquad \text{(Eq 5)}$$

where:

V_T is the pickup voltage at the base reach angle given by equation 4.

A is the base reach angle in degrees

B is the polarizing voltage phase shift setting in degrees (0 or 15)

C is the characteristic timer setting in degrees. For 50 Hz relays multiply the timer setting in milliseconds by 18 to obtain degrees; for 60 Hz relays multiply by 21.6

D is the phase angle meter reading (voltage leading current)

2B) Pickup for angles greater than the base reach angle.

a) Adjust the phase shifter to obtain a phase angle 30 degrees greater than the base reach angle.

b) Lower the applied voltage and check for an MT output within five percent of the voltage given by the expression:

$$V_D = V_T \times \frac{\text{SIN } (D - A + B + C)}{\text{SIN } (B + C)} \qquad \text{(Eq 6)}$$

Complete MOB Function Check

The following procedure is recommended to provide an overall check of the MOB function. Note that the MT and MOB functions have common circuits and adjustments for all settings except the characteristic timer settings. The same procedure is used for the MOB check as for the MT check outlined in subsection COMPLETE MT FUNCTION CHECK with the following exceptions. The output of the MOB function should be monitored at TP3. Pin 8 on the coincidence logic cards for the phase pairs not under test should be jumpered to reference; refer to Table VIII for the cards involved.

TABLE VIII

PHASE PAIR UNDER TEST	JUMPER PIN 8 TO REF ON CARDS
A-B	M and N
B-C	L and N
C-A	L and M

PERIODIC CHECKS AND ROUTINE MAINTENANCE

PERIODIC CHECKS

The MT functions included in the relay may be checked at periodic intervals using the procedures described under OVERALL CHECK OF THE MT FUNCTION. By checking the reach of each unit at the base reach angle and one angle either side of the base reach angle, all settings of the MT functions may be verified.

TROUBLESHOOTING

In any troubleshooting equipment, it should first be established which unit is functioning incorrectly. The overall logic diagram supplied with the equipment shows the combined logic of the complete equipment and the various test points in each unit. By signal tracing, using the overall logic diagram and the various test points, it should be possible to quickly isolate the trouble.

A test adapter card (0149C7259G-2) is supplied with each static relay equipment to supplement the prewired test points on the test cards. Use of the adapter card is described in the card instruction book GEK-34158.

A dual-trace oscilloscope is a valuable aid to detailed troubleshooting, since it can be used to determine phase shift, operate and reset times as well as input and output levels. A portable dual-trace oscilloscope with a calibrated sweep and trigger facility is recommended.

SPARE PARTS

To minimize possible outage time, it is recommended that a complete maintenance program should include the stocking of at least one spare card of each type. It is possible to replace damaged or defective components on the printed circuit cards, but great care should be taken in soldering so as not to damage or bridge-over the printed circuit busses, or overheat the semi-conductor components. The repaired area should be recovered with a suitable high-dielectric plastic coating to prevent possible breakdowns across the printed busses due to moisture and dust. The wiring diagrams for the cards in the relay are included in the card book GEK-34158; the card types are shown on the component location diagram, Fig. 3.

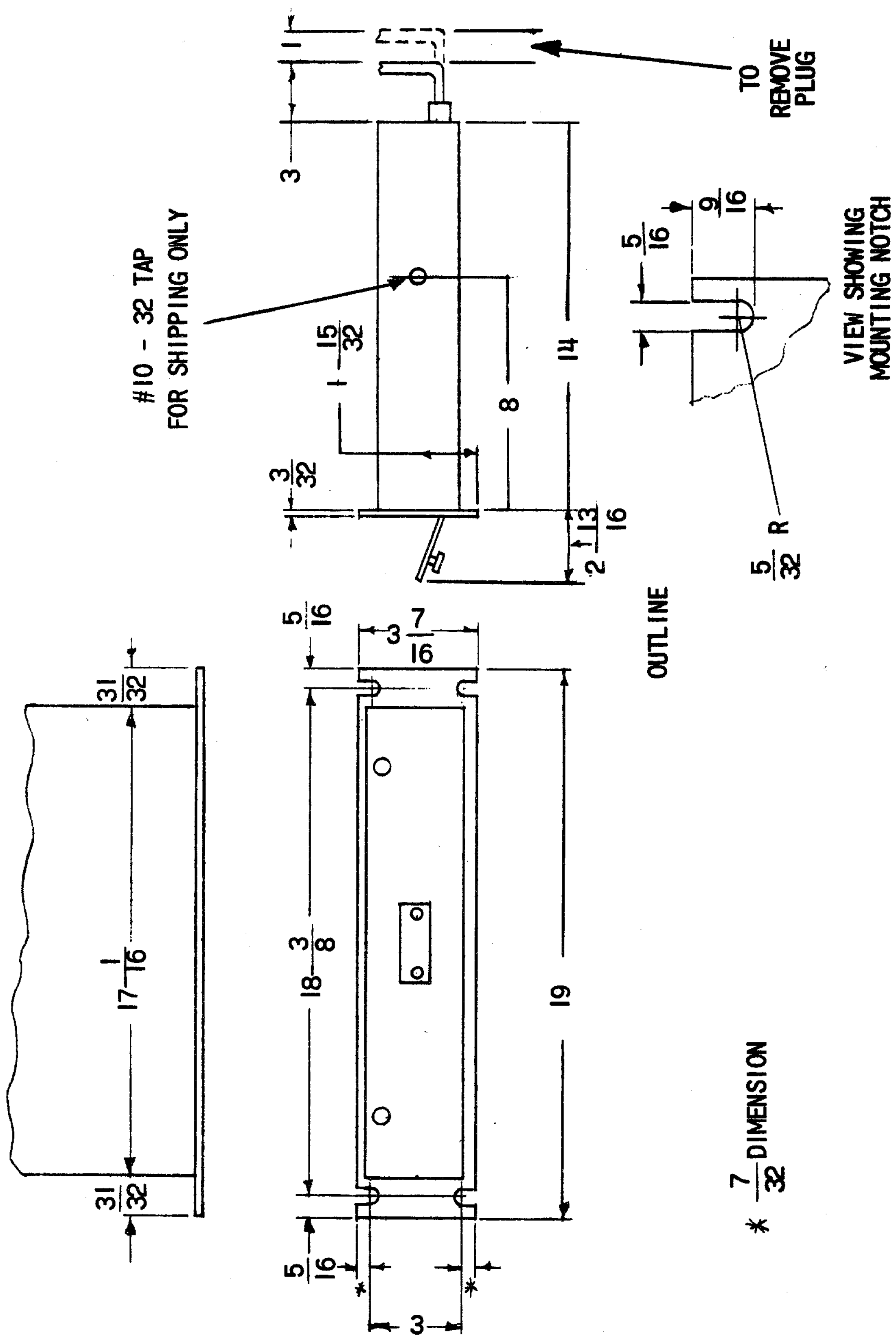


Fig. 1 (0227A2036-0) OUTLINE AND MOUNTING DIMENSIONS FOR THE SLY62A RELAY

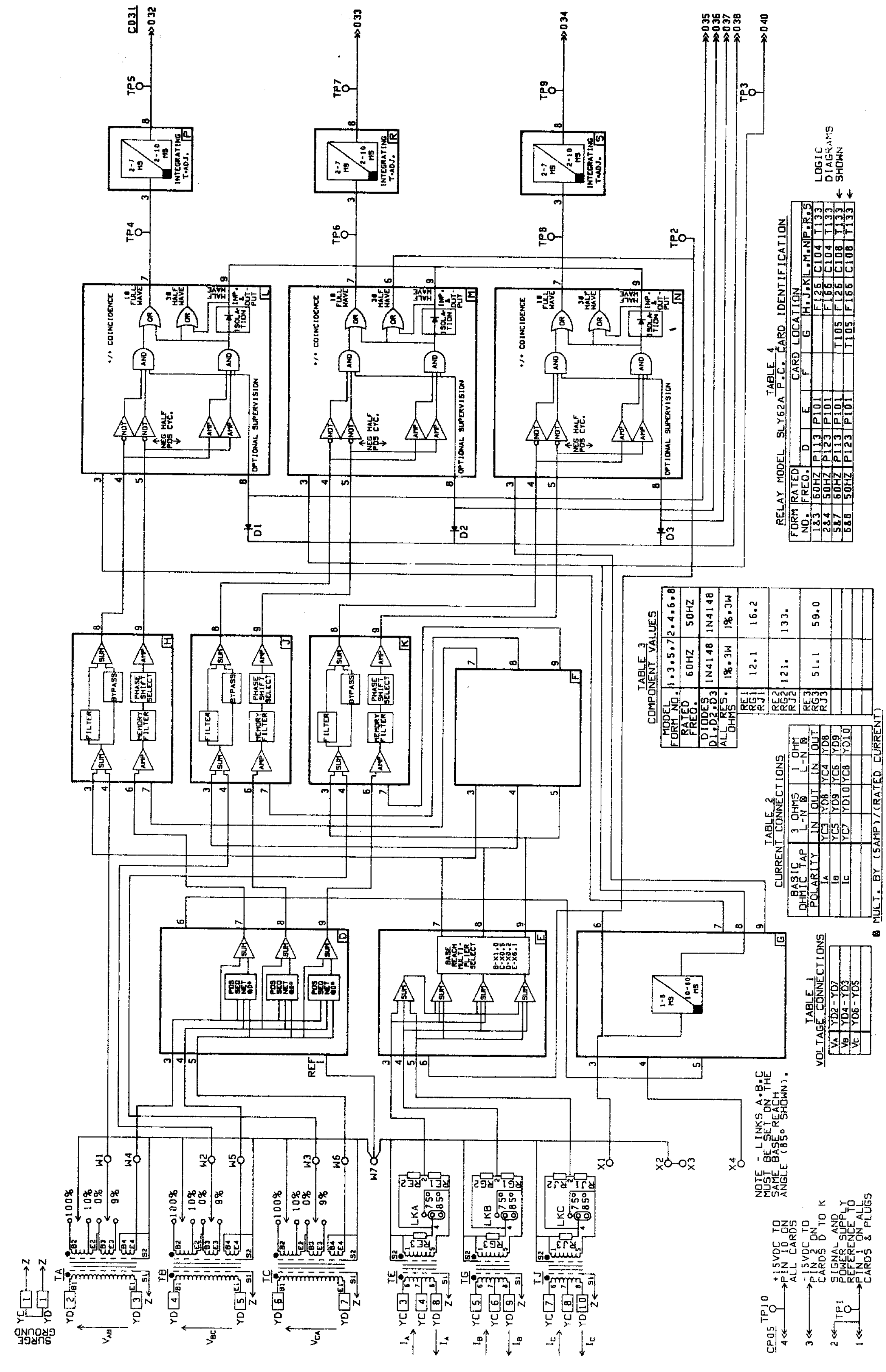


Fig. 2 (0136D4921-0) INTERNAL CONNECTION DIAGRAM FOR THE SLY62A RELAY

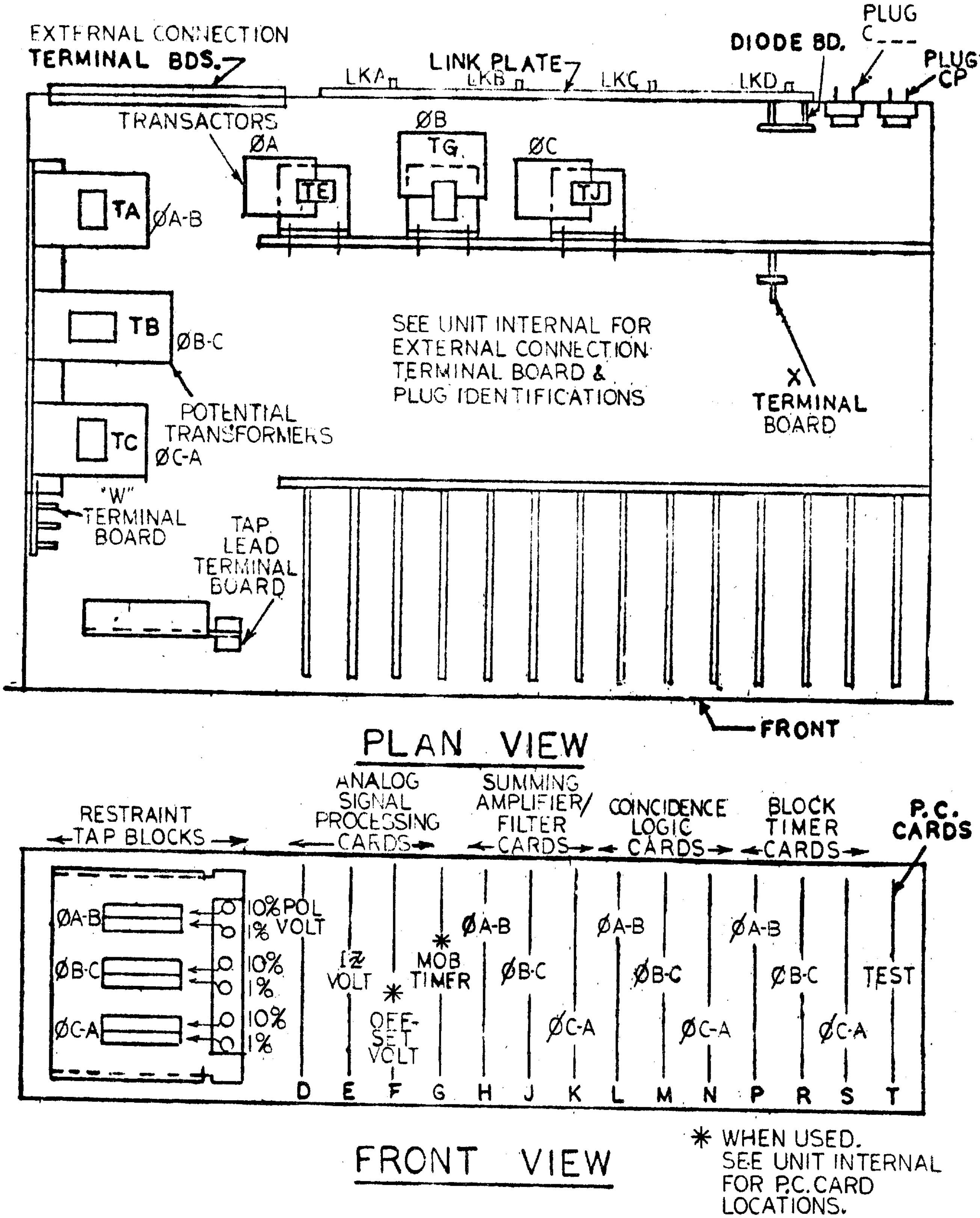
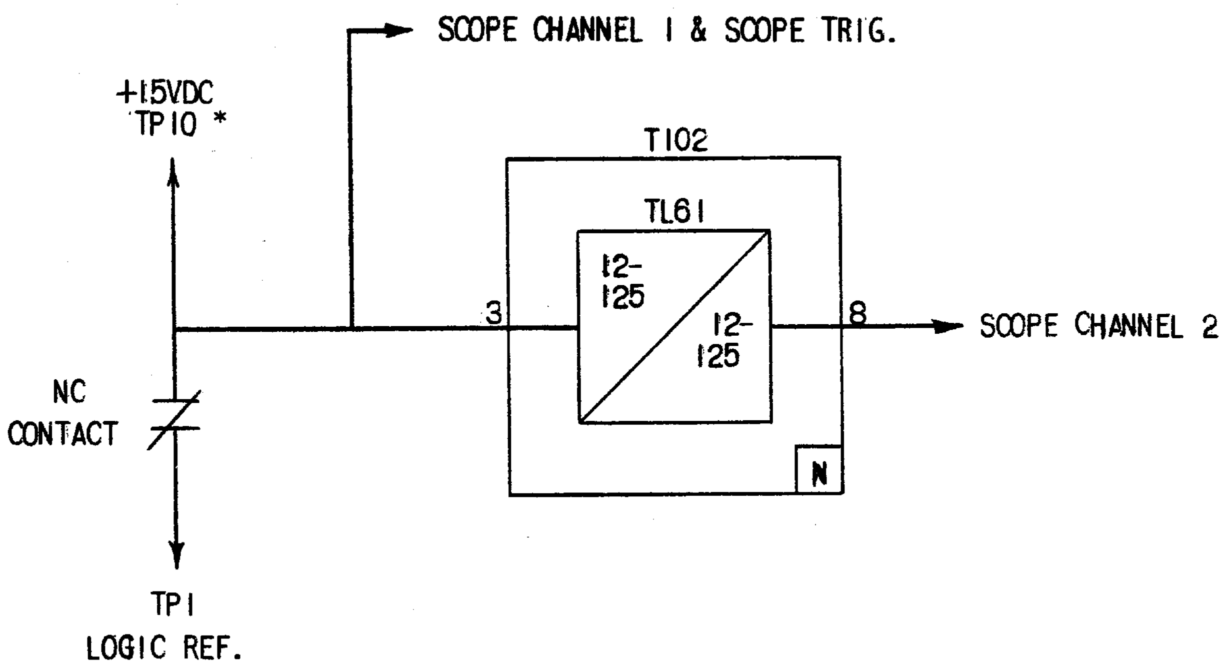


Fig.3 (0269A3157-1) COMPONENT LOCATION DIAGRAM FOR THE SLY62A RELAY



* THE 15VDC SIGNAL AT PIN 10 HAS A CURRENT LIMITING RESISTOR MOUNTED ON THE TEST CARD.

Fig. 4 (0246A7987-0) TIMER TEST CIRCUIT

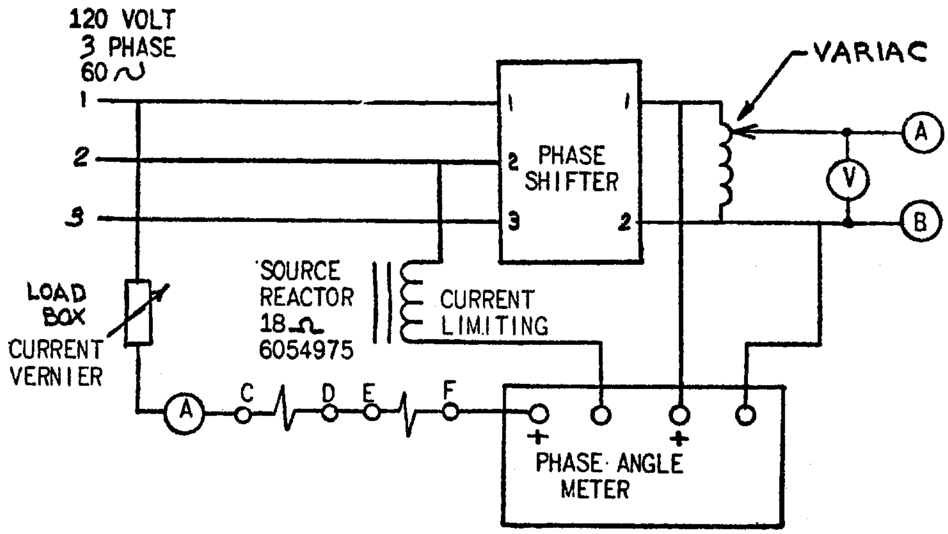


Fig. 5 (0178A7029-3) MT/MOB FUNCTION TEST CIRCUIT

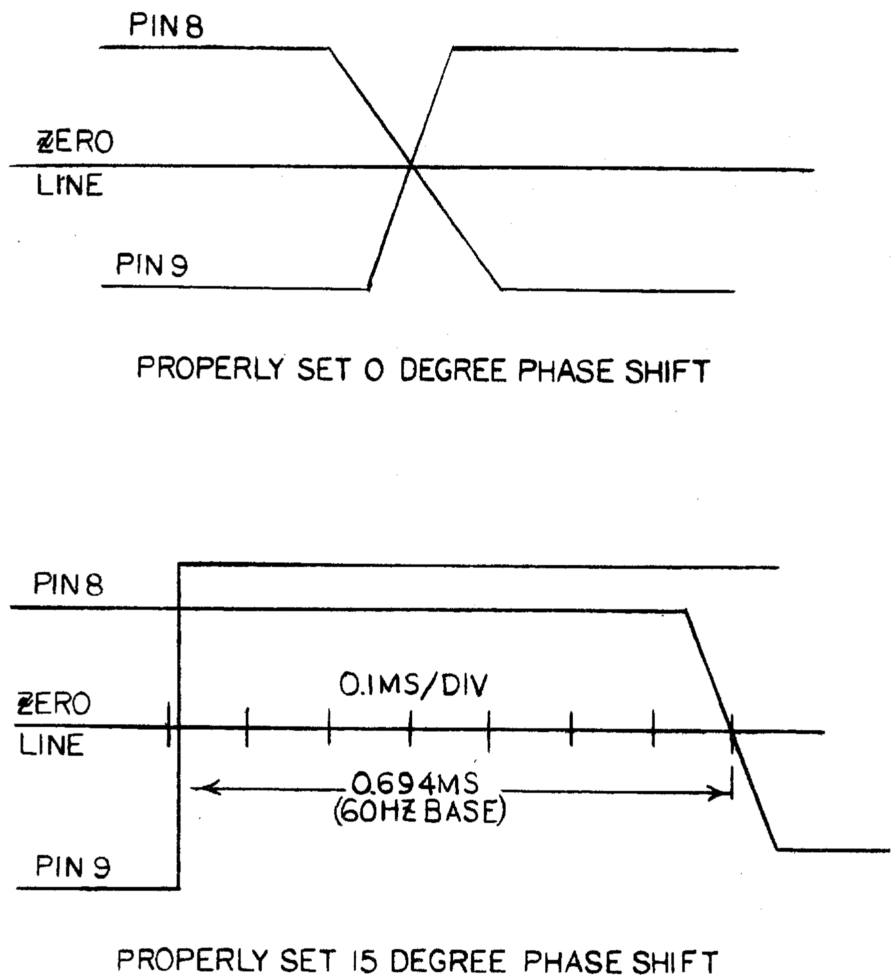


Fig. 6 (0257A9696-1) POLARIZING CIRCUIT PHASE SHIFT WAVEFORMS

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